

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Patent Application of:

Inventor(s): Philip E. May et al.
Filed: August 29, 2003
Serial No.: 10/652,135
Confirmation No.: 1155
Group Art Unit: 2181
Examiner: MEONSKE, Tonia L.
Docket Number: CML00769D
Title: METHOD AND APPARATUS FOR ELIMINATION
OF PROLOG AND EPILOG INSTRUCTIONS IN A
VECTOR PROCESSOR

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF EFS TRANSMISSION

I hereby certify that this correspondence is, on the date shown below, being:
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[] transmitted by facsimile to the U.S. Patent and Trademark Office to Examiner:
at Fax Number: _____
RENEE' MICHELLE LEVEQUE /Renee' Michelle Leveque/ May 2, 2007
(Applicant, Assignee or Reg. Representative)Signature Date

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Dear Sir:

In response to the Restriction/Election Requirement mailed on April 6, 2007,
Applicant provisionally elects the invention identified at species Invention II., claims
13-24, drawn to loop execution control.

All claims 1-24 are believed to be in allowable condition and such allowance
is respectfully requested at the Examiner's earliest convenience. The Examiner is
cordially invited to contact the undersigned if there are any questions about this
application or response.

Respectfully submitted,

/Renee' Michelle Leveque/

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